



PRODUCT INFORMATION

SFP / SFP+

Optical Transceiver

The hot pluggable SFP+ transceiver is a high performance, cost effective duplex LC module interface. The receiver section uses a PIN receiver, and the transmitter uses a VCSEL laser, with up to 9dB link budget to ensure a 1000Base ethernet up to 550M application.

Features

- Up to 40Gb/s
- Duplex LC connector
- Maximum Power <1W
- Industrial /Extended/ Commercial operating temperature range: -40°C to 85° C/-5°C to 85°C/- 0°C to 70°C Version available
- RoHS compliant and Lead Free

Applications

- Metro/Access Networks
- Up to 40Gb/s 1000Base-SX Ethernet 1×Fibre Channel
- Other Optical Links

Absolute Maximum Ratings

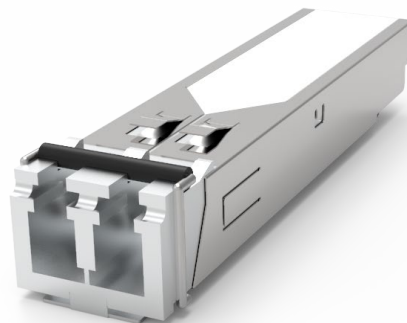
Storage Temperature (TS)	-40°C (Min.)		+85°C (Max.)
Supply Voltage (VCC)	-0.5V (Min.)		4V (Max.)
Relative Humidity (RH)	85% (Max.)		

Recommended Operating Environment

Case Operating Temperature

Industrial (TC)	-40°C (Min.)		85°C (Max.)
Extended (TC)	-5°C (Min.)		85°C (Max.)
Commercial (TC)	+70°C (Max.)		

Supply Voltage (VCC)	3.135V (Min.)		3.465V (Max.)
Supply Current (ICC)	300mA (Max.)		
Inrush Current (Isurge)	ICC+30mA (Max.)		
Maximum Power (Pmax)	1W (Max.)		



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Electrical Characteristics (TC=-40°C to 85°C, VCC=3.135V to 3.465V)

Transmitter Section

Input Differential Impedance (Rin)¹	90 (Min.) 110 (Max.)		100 (Typical)
Single Ended Data Input Swing (Vin PP)	250mVp-p (Min.)		1200mVp-p (Max.)
Transmit Disable Voltage (VD)²	VCC-1.3V (Min.)		VCC
Transmit Enable Voltage (VEN)	Vee (Min.)		Vee+0.8V
Transmit Disable Assert Time (Tdessert)	10us (Max.)		

Receiver Section

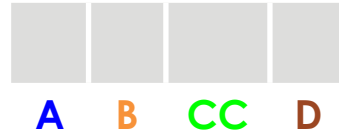
Single Ended Data Output Swing (Vout PP)³	250mv (Min.)		800mv (Max.)
LOS Fault (Vlosfault)⁵	VCC-0.5V (Min.)		VCC_host
LOS Normal (Vlos norm)⁵	Vee (Min.)		Vee+0.5V
Power Supply Rejection (PSR)⁶	100mVpp (Min.)		

Notes:

1. AC coupled.
2. Or open circuit.
3. Into 100 ohm differential termination.
4. 20 - 80 %
5. LOS is LVTTTL. Logic 0 indicates normal operation; logic 1 indicates no signal detected.
6. All transceiver specifications are compliant with a power supply sinusoidal modulation of 20 Hz to 1.5MHz up to specified value applied through the power supply filtering network shown on page 23 of the Small Form-factor Pluggable (SFP) Transceiver Multi-Source Agreement (MSA), September 14, 2000.

Part Number Configurator

SFP-



A	Select Transmission	D	Select Distance
C	155Mbps (10/100 Base)	1	10km (Single-mode)
M	1.25G	T	25km (Single-mode)
G	10G	4	40km (Single-mode)
T	25G	F	50km (Single-mode)
4	40G	8	80km (Single-mode)
B	Select Fiber Type	5	550m (Multimode)
Y	Single-mode	2	2km (Multimode)
F	Multimode		
X	Empty slot		
CC	Select Operating Wavelength		
10	1310nm		
15	1550nm (Single-mode)		
13	1300nm (Multimode)		
85	850nm (Multimode)		

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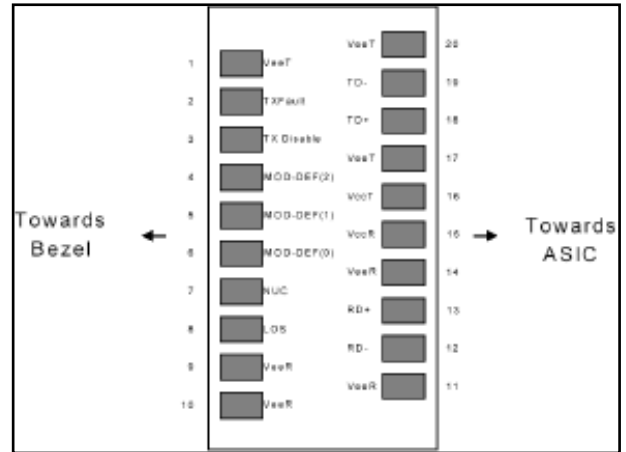
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Pin Assignment

Diagram of Host Board Connector Block Pin Numbers and Name



Pin Function Definitions

Pin No	Name	Function	Plug Seq	Notes
1	VeeT	Transmitter Ground	1	1
2	TX Fault	Transmitter Fault Indication	3	
3	TX Disable	Transmitter Disable	3	2
4	MOD-DEF2	Module Definition	2	3
5	MOD-DEF1	Module Definition 1	3	3
6	MOD-DEF0	Module Definition 0	3	3
7	Rate Select	Not Connected	3	4
8	LOS	Loss of Signal	3	5
9	VeeR	Receiver Ground	1	1
10	VeeR	Receiver Ground	1	1
11	VeeR	Receiver Ground		1
12	RD-	Inv. Received Data Out	3	6
13	RD+	Received Data Out	3	6
14	VeeR	Receiver Ground	3	1
15	VccR	Receiver Power	2	1
16	VccT	Transmitter Power	2	
17	VeeT	Transmitter Ground	1	
18	TD+	Transmit Data In	3	6
19	TD-	Inv. Transmit In	3	6
20	VeeT	Transmitter Ground	1	

Notes:

1. Circuit ground is internally isolated from chassis ground.
2. Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
3. Should be pulled up with 4.7k - 10 kohms on host board to a voltage between 2.0V and 3.6V. MOD_DEF(0) pulls line low to indicate module is plugged in.
4. Rate select is not used.
5. LOS is open collector output. Should be pulled up with 4.7k - 10 kohms on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.
6. AC coupled.

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SFP Module EEPROM Information and Management

The SFP modules implement the 2-wire serial communication protocol as defined in the SFP - 8472. The serial ID information of the SFP modules and Digital Diagnostic Monitor parameters can be accessed through the I2C interface at address A0h and A2h. The memory is mapped in Table 1. Detailed ID information (A0h) is listed in Table 2. And the DDM specification at address A2h. For more details of the memory map and byte definitions, please refer to the SFF-8472, "Digital Diagnostic Monitoring Interface for Optical Transceivers". The DDM parameters have been internally calibrated.

Table 1. Digital Diagnostic Memory Map (Specific Data Field Descriptions)

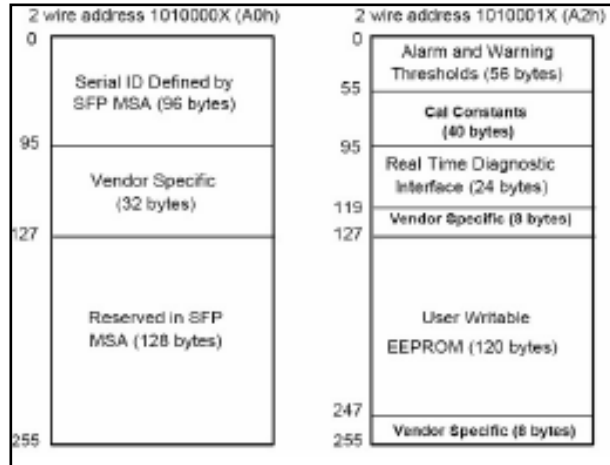


Table 2 - EEPROM Serial ID Memory Contents (A0h)

Data Address	Length (Byte)	Name of Length	Description and Contents
Base ID Fields			
0	1	Identifier	Type of Serial transceiver (03h=SFP)
1	1	Reserved	Extended identifier of type serial transceiver (04h)
2	1	Connector	Code of optical connector type (07=LC)
3-10	8	Transceiver	
11	1	Encoding	NRZ(03h)
12	1	BR, Nominal	Nominal baud rate, unit of 100Mbps
13-14	2	Reserved	(0000h)
15	1	Length(9um)	Link length supported for 9/125um fiber, units of 100m
16	1	Length(50um)	Link length supported for 50/125um fiber, units of 10m
17	1	Length(62.5um)	Link length supported for 62.5/125um fiber, units of 10m
18	1	Length(Copper)	Link length supported for copper, units of meters
19	1	Reserved	
20-35	16	Vendor Name	
36	1	Reserved	
37-39	3	Vendor OUI	SFP transceiver vendor OUI ID
40-55	16	Vendor PN	
56-59	4	Vendor rev	Revision level for part number
60-62	3	Reserved	
63	1	CCID	Least significant byte of sum of data in address 0-62

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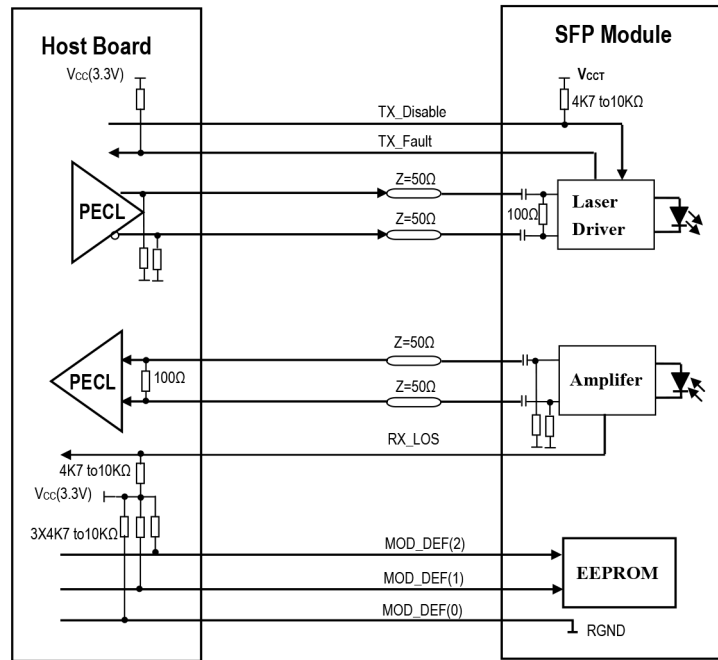
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Data Address	Length (Byte)	Name of Length	Description and Contents
Extended ID Fields			
64-65	2	Option	Indicates which optical SFP signals are implemented (001Ah = LOS, TX_FAULT, TX_DISABLE all supported)
66	1	BR, max	Upper bit rate margin, units of %
67	1	BR, min	Lower bit rate margin, units of %
68-83	16	Vendor SN	Serial number (ASCII)
84-91	8	Date code	Manufacturing date code
92-94	3	Reserved	
95	1	CCEX	Check code for the extended ID Fields (addresses 64 to 94)
Vendor Specific ID Fields			
96-127	32	Readable	specific date, read only
128-255	128	Reserved	

Recommended Circuit



SFP Host Recommended Circuit

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Data Address	Parameter	Accuracy	Unit
96-97	Transceiver Internal Temperature	±3.0	°C
100-101	Laser Bias Current	±10	%
100-101	Tx Output Power	±3.0	dBm
100-101	Rx Input Power	±3.0	dBm
100-101	VCC3 Internal Supply Voltage	±3.0	%

Regulatory Compliance

This SFP complies with international Electromagnetic Compatibility (EMC) and international safety requirements and standards (see details in the following table).

Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883E Method 3015.7	Class 1 (>1000 V)
Electrostatic Discharge (ESD) to the Duplex LC Receptacle	IEC 61000-4-2 GR-1089-CORE	Compatible with standards
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN55022 Class B (CISPR 22B) VCCI Class B	Compatible with standards
Laser Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN60950, EN (IEC) 60825-1,2 product.	Compatible with Class 1 laser

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